IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

DE ME	En re PATEN	Γ APPLICATION of Hiroki GOKO	Group Art Unit: 2816 Examiner: Unassigned
PATENT & TO ATE	Applicant:	Hiroki GOKO)
	Appl. No.:	10/649,931)
	Filed :	August 28, 2003) INFORMATION) DISCLOSURE) STATEMENT
	For :	SEMICONDUCTOR INTEGRATED CIRCUIT) <u>STATEMENT</u>)
	Atty. Dkt. :	OKI 369)
	Commissione P.O. Box 145 Alexandria, V		

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(3), i.e., prior to a first Office Action on the merits.

Attached is a copy of one publication, which is listed on the attached Form PTO-1449. Also attached is an English-language abstract from which any relevance of the publication may be gleaned.

Since this Information Disclosure Statement is being filed before the first Office Action, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to all documents submitted. Consideration of the submitted documents is respectfully requested.

Should any fee be required, please charge the same to our Deposit Account No. 18-0002 and advise us accordingly.

December 10, 2003

Date

Steven M. Rabin - Reg. No. 29,102

RABIN & BERDO, P.C.

Respectfully submitted,

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CUSTOMER NO. 23995

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT OF CONTROL OF CON				OKI 369						
				Applicant Hiroki GOKI						
				Filing Date August 28, 2003	Group Unit					
U.S. PATENT DOCUMENTS										
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